

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	5	bypass\$3 near5 (programmable adj2 divider)	US-PGPUB; USPAT	2006/03/23 16:03
2	BRS	L2	166	(divid\$3 near3 register) with mode	US-PGPUB; USPAT	2006/03/23 16:05
3	BRS	L3	5950	programmable adj2 (divider counter)	US-PGPUB; USPAT	2006/03/23 16:06
4	BRS	L4	16	2 and 3	US-PGPUB; USPAT	2006/03/23 16:32
5	BRS	L5	5	divid\$3 adj2 mode adj2 register	US-PGPUB; USPAT	2006/03/23 16:45
6	BRS	L6	1	divmode	US-PGPUB; USPAT	2006/03/23 16:48
7	BRS	L7	126644	performance same power	US-PGPUB; USPAT	2006/03/23 16:49
8	BRS	L8	20	2 and 7	US-PGPUB; USPAT	2006/03/23 17:02
9	BRS	L9	570	3 and 7	US-PGPUB; USPAT	2006/03/23 17:02
10	BRS	L10	5566	tun\$3 near3 performance	US-PGPUB; USPAT	2006/03/23 17:02
11	BRS	L11	80	9 and 10	US-PGPUB; USPAT	2006/03/23 17:03
12	BRS	L12	62	11 and computer	US-PGPUB; USPAT	2006/03/23 17:03
13	BRS	L13	62	12 not (1 or 4 or 8)	US-PGPUB; USPAT	2006/03/23 17:04
14	BRS	L14	62	13 and mode	US-PGPUB; USPAT	2006/03/23 17:04
15	BRS	L15	44287	power near3 (sav\$3 conserv\$3)	US-PGPUB; USPAT	2006/03/23 17:05

	Type	L #	Hits	Search Text	DBs	Time Stamp
<b>16</b>	BRS	L16	2	14 and 15	US-PGPUB; USPAT	2006/03/23 17:09
<b>17</b>	BRS	L17	2055	713/300.ccls.	US-PGPUB; USPAT	2006/03/23 17:09
<b>18</b>	BRS	L18	0	14 and 17	US-PGPUB; USPAT	2006/03/23 17:09
<b>19</b>	BRS	L19	1150	713/320.ccls.	US-PGPUB; USPAT	2006/03/23 17:10
<b>20</b>	BRS	L20	0	14 and 19	US-PGPUB; USPAT	2006/03/23 17:10